

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: John Bruno

Examiner: Nathan W. Ha

Serial No.: 10/064,856

Art Unit: 2814

Filing Date: August 23, 2002

Docket No.: 00100.02.0038

Confirmation No.: 4595

Title: **INTEGRATED CIRCUIT HAVING MEMORY DISPOSED THEREON
AND METHOD OF MAKING THEREOF**

Mail Stop Issue Fee
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

ISSUE FEE TRANSMITTAL


Dear Sir:

Enclosed herewith please find:

- ☒ Form PTOL-85 dated January 31, 2006, in duplicate; and
- ☒ "Fee Address" Indication Form.

Respectfully submitted,

Date: 5/15/06

By: 
Christopher J. Reckamp
Registration No. 34,414

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